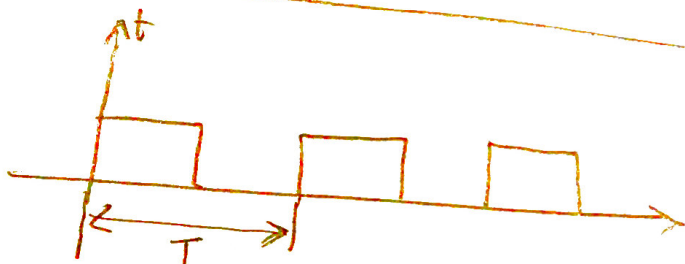
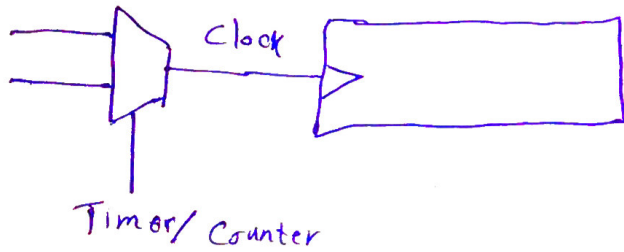
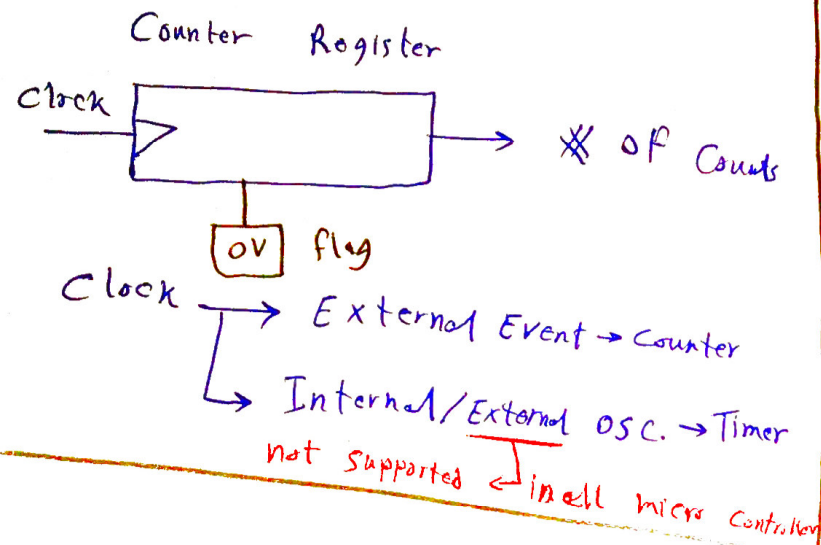


Timer / Counters



$$\text{time} = \underbrace{n}_\substack{\text{* of Counts}} T$$

Over Flow flag is visid
when counting up from
FFH to 00H

$$\text{Counter} \leftarrow 256 - n$$

↳ 8-bit ↳ wanted number of Counts
↳ Value in register

If we want to count
number of event occurrence
we initialize the counter
with 00H

Dealing with OV flag

- 1- check the OV flag
Periodically (has many cons.)
- 2- Using Interrupt (Timer Interrupt)

We need to reset OV flag
in software after interrupt

in Software / hardware

- 1- We need to know
Clock Source
- 2- We need to know how
to access the register
- 3- We need to know how
to configure it as Timer
or Counter

Registers

- 1- Timer Register
 - 2- Port for clock (Counter)
 - 3- Option Register (Timer / counter)
(Internal / external)
 - 4- Interrupt Register
- ↳ Flag ↳ interrupt

option register → Timer / Counter
→ Timer mode
→ Prescaler → For how
number of osc. clock
cycle we trigger the timer

timer 0

8-bit timer

timer 1

16 bit timer

timer 2

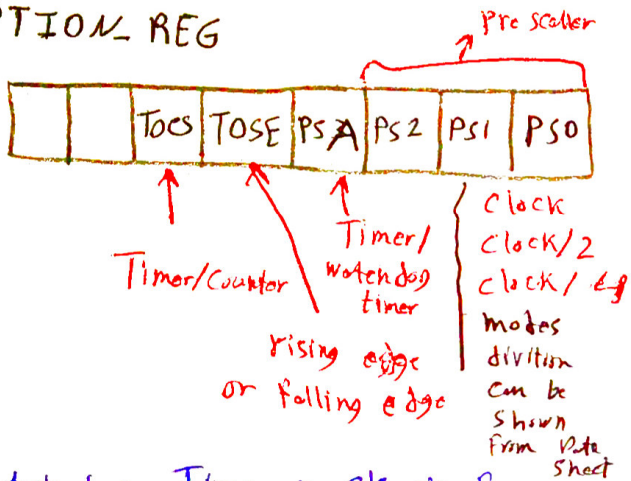
8 bit timer

timero

PIC

TMRO : timer register

OPTION REG



Watch dog Timer → check for

Failure or infinity Loop
with no actions

INTCON : Interrupt register



→ required to be 1 to Enable any Interrupt

PEIE → Peripheral interrupt → 1

TOIE → Timer Zero Interrupt Enable

TOIF → Timer Zero Interrupt Flag

Task

Push Button

2 LED

- One Led Blinking each 5 second
- other on When pressing Push Button

Void interrupt(Void) {

// check Flag

}